Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	37	mega-cell or megecell	USPAT	OR	OFF	2005/05/31 13:05
L2	47	mega-cell or megecell	USPAT	OR	ON	2005/05/31 13:05
L3	336	mega-cell or megacell	USPAT	OR	ON	2005/05/31 13:05
L4	85	3 and (power adj consumption)	USPAT	OR	ON	2005/05/31 13:05
L5	34	4 and simulation	USPAT	OR	ON	2005/05/31 13:05
L6	2	5 and ((power adj consumption) same simulation)	USPAT	OR	ON	2005/05/31 13:06
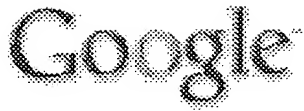
Google "simulating logic of megacells"   [Search]   Advanced Search
Preferences

Web

Tip: Try removing quotes from your search to get more results.

Your search - **"simulating logic of megacells"** - did not match any documents.

Suggestions:

- Make sure all words are spelled correctly.
- Try different keywords.
- Try more general keywords.

Google Home - Advertising Programs - Business Solutions - About Google

Google API Proximity Search (GAPS)
From staggernation.com - Read Me - GAPS - GARBO - GAWSH

Find `simulation` **within** `3 ▼` **word(s) of** `megacell`   `Search`

`in that order ▼` `Sort by title ▼` **Additional terms:** `_____`

Show `All ▼` results, with up to `10 ▼` from each query ☐ Filter each query

License key
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`_____`

Found simulation within 3 words of megacell.   First 3 results of about 3 from 4 queries.

An I/O System on a Chip [distance: 2]
... rent **simulation of the megacell** and the rest of the chip. Be. cause the megacell
vendor used proprietary simulators run. ning in a mainframe environment ...
http://www.hpl.hp.com/hpjournal/95apr/apr95a4.pdf - - Cached - Similar pages

Fast Prototyping: a system design flow for fast design ... [distance: 3]
... To get a **simulation model of the megacell** we also need the. DSP, represented
by its ISS integrated in the design. The. ISS is generated using FlexWare ...
http://www.hwswworld.com/downloads/y13/3_4.PDF - - Cached - Similar pages

IP-based System-On-Chip Design [distance: 2]
... Master-Slave Interaction. Multilayer IP Model. Heterogeneous Co-Simulation.
HW-SW Co-**Simulation. ADSL Megacell**. Outline. Effective SOC Implementation ...
http://www.cn.nctu.edu.tw/faculty/lennon/Talks/ECE-talk1/ - 5k - Cached - Similar pages

Google API Proximity Search (GAPS)

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Find simulation **within** 2 ▾ **word(s) of** megacell   Search

in that order ▾   Sort by title ▾   Additional terms:

Show All ▾ results, with up to 10 ▾ from each query   ☐ Filter each query

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Found simulation within 2 words of megacell.

First 2 results of about 2 from 3 queries.

An I/O System on a Chip [distance: 2]

... rent **simulation of the megacell** and the rest of the chip. Be. cause the megacell vendor used proprietary simulators run. ning in a mainframe environment ...
http://www.hpl.hp.com/hpjournal/95apr/apr95a4.pdf - - Cached - Similar pages

IP-based System-On-Chip Design [distance: 2]

... Master-Slave Interaction. Multilayer IP Model. Heterogeneous Co-Simulation. HW-SW Co-**Simulation**. **ADSL Megacell**. Outline. Effective SOC Implementation ...
http://www.cn.nctu.edu.tw/faculty/lennon/Talks/ECE-talk1/ - 5k - Cached - Similar pages

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Find simulation **within** 2 ▾ **word(s) of** megacell   Search

in that order ▾   Sort by title ▾   Additional terms:

Show All ▾ results, with up to 10 ▾ from each query   ☐ Filter each query

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